Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1	("5659748").PN.	USPAT	OR	OFF	2005/11/27 16:59
L2	2	"083157"	US-PGPUB	OR	OFF	2005/11/27 15:56
L4	17	(start$ or power or BIOS) same (transfer near3 data) same rewritable same memory	USPAT	OR	ON	2005/11/27 16:01
L5	0	("1and(datanear3transfer)").PN.	USPAT	OR	OFF	2005/11/27 17:00
L6	0	1 and (data near3 transfer)	USPAT	OR	OFF	2005/11/27 17:00
L7	1	1 and (data with transfer)	USPAT	OR	OFF	2005/11/27 17:01
L8	0	1 and (transfer with alternate)	USPAT	OR	OFF	2005/11/27 17:01
L9	1	1 and (transfer$ with alternat$)	USPAT	OR	OFF	2005/11/27 17:26
L10	0	1 and (address near3 memory)	USPAT	OR	OFF	2005/11/27 17:26
L11	1	1 and (address near5 memory)	USPAT	OR	OFF	2005/11/27 17:29
L12	1	1 and (serial same interface)	USPAT	OR	OFF	2005/11/27 17:56
L14	1285	(computer or processor or cpu) same (data adj transfer) same ROM same memory	USPAT	OR	ON	2005/11/27 17:57
L15	120	14 same BIOS	USPAT	OR	ON	2005/11/27 18:02
L17	192	713/1.ccls. and (data adj transfer)	USPAT	OR	OFF	2005/11/27 18:44
L18	173	17 and (ROM or memory) same (CPU or computer or processor)	USPAT	OR	OFF	2005/11/27 18:43

L19	49	17 and ((ROM or memory) same (CPU or computer or processor)).ab.	USPAT	OR	ON	2005/11/27 18:45
L22	73	712/28.ccls. and (data adj transfer)	USPAT	OR	OFF	2005/11/27 18:45
L23	6986	"122" and ((ROM or memory) same (CPU or computer or processor)). ab.	USPAT	OR	ON	2005/11/27 18:45
L24	38	22 and ((ROM or memory) same (CPU or computer or processor)).ab.	USPAT	OR	ON	2005/11/27 18:45
L25	71	712/35.ccls. and (data adj transfer)	USPAT	OR	OFF	2005/11/27 18:45
L26	38	25 and ((ROM or memory) same (CPU or computer or processor)).ab.	USPAT	OR	ON	2005/11/27 18:46